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Term	Documents
MC68302.USPT.	61
MC68302S	0
INTERFACE.USPT.	243183
INTERFACES.USPT.	63072
(MC68302 SAME INTERFACE).USPT.	36

Database: US Patents Full-Text Database

Refine Search:

mc68302 same interface**Search History**

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT	mc68302 same interface	36	<u>L3</u>
USPT	mc68302 and interface	57	<u>L2</u>
USPT	interface same (gate array) same Asic	94	<u>L1</u>

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Searches for User *athompson* (Count = 664)

Queries 615 through 664.

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S # Comment Database

Query

<u>S664</u>	USPT	((interface\$ buffer\$) and fpga) and mux
<u>S663</u>	USPT	(interface\$ buffer\$) and fpga
<u>S662</u>	USPT	(interface\$ buffer\$) same fpga
<u>S661</u>	USPT	interface\$ buffer\$
<u>S660</u>	USPT	((mask programmable and rout\$) and interface) and field programmable
<u>S659</u>	USPT	(mask programmable and rout\$) and interface
<u>S658</u>	USPT	mask programmable and rout\$
<u>S657</u>	USPT	5128831.pn. or 4953060.pn.
<u>S656</u>	USPT	mc68302 same interface
<u>S655</u>	USPT	mc68302 and interface
<u>S654</u>	USPT	interface same (gate array) same Asic
<u>S653</u>	USPT	5680332.pn. or 5537580.pn. or 5513122.pn. or 5469367.pn. or 5140526.pn.
<u>S652</u>	USPT	5920711.pn.
<u>S651</u>	USPT	5058026.pn. or 5980084.pn.
<u>S650</u>	USPT	((automated assembly) and disassembly) and search and collision
<u>S649</u>	USPT	((automated assembly) and disassembly) and search
<u>S648</u>	USPT	((automated assembly) and disassembly) and search strategy
<u>S647</u>	USPT	((automated assembly) and disassembly) and collision detection
<u>S646</u>	USPT	(automated assembly) and disassembly
<u>S645</u>	USPT	automated assembly
<u>S644</u>	USPT	face same module generation
<u>S643</u>	USPT	face same design
<u>S642</u>	USPT	face design
<u>S641</u>	USPT	((fpga same map\$) and placement) and modula\$
<u>S640</u>	USPT	(fpga same map\$) and placement
<u>S639</u>	USPT	fpga same map\$

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT	19 and mux\$	4	<u>L11</u>
USPT	18 and mux\$	12	<u>L10</u>
USPT	17 and fpga and asic	24	<u>L9</u>
USPT	17 and gate array and asic	36	<u>L8</u>
USPT	16 and iob	417	<u>L7</u>
USPT	15 and asic	59	<u>L6</u>
USPT	11 and interconnect	306	<u>L5</u>
USPT	13 and mux	37	<u>L4</u>
USPT	11 and fpga	44	<u>L3</u>
USPT	11 same fpga	1	<u>L2</u>
USPT	interface\$ buffer\$	1034	<u>L1</u>